

Electronic Acknowledgement Receipt

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International Application Number:	
Confirmation Number:	7316
Title of Invention:	System and method for balancing computational load across a plurality of processors
First Named Inventor/Applicant Name:	Barry L. Minor
Customer Number:	40412
Filer:	Joseph Tyler VanLeeuwen
Filer Authorized By:	
Attorney Docket Number:	AUS920030790US1
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Application Type:	Utility under 35 USC 111(a)

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File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
1	NPL Documents	IBMDictionaryofComputing_PA.pdf	272958 de2ca3e97ed3293f501aaf9cf12794cf53046636	no	5

Warnings:

Information:

2		AUS920030790US1_IDSPT O1449_12-27-07.pdf	96534 <small>47132127c008eb63f3f72b2b328f10ab 35fe2a5</small>	yes	3
	Multipart Description/PDF files in .zip description				
	Document Description		Start	End	
	Information Disclosure Statement Letter		1	1	
	Information Disclosure Statement (IDS) Filed		2	3	
Warnings:					
Information:					
Total Files Size (in bytes):			369492		
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